AL-AZHAR COLLEGE OF ENGINEERING AND TECHNOLOGY,THODUPUZHA

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

S5 CSE 2020-2024 Batch

MONTHLY ATTENDANCE-OCTOBER 2022

Sl.No:	Register Numbe	Student Name	CST 301		CST 303		CST 305		CST 307		CST 309		MCN 301		CSL 333		CSL 331	
			No:of days present	Attendance Percentage	No:of days present	Attendance Percentage	No:of days present	Attendance Percentage	No:of days present	Attendance Percentage	No:of days present	Attendance Percentage	No:of days present	Attendance Percentage	No:of days present	Attendance Percentage	No:of days present	Attendance Percentage
			Total:23		Total: 20		Total:16		Total: 15		Total:16		Total:7		Total:3		Total:5	
1	AAE20CS001	ADITYA SURESH	14	61	9	45	9	56	11	73	10	63	4	57	2	67	3	60
2	AAE20CS002	AKHIL NATH T M	11	48	6	30	8	50	9	60	11	69	4	57	3	100	2	40
3	AAE20CS004	ALFIYA E S	16	70	14	70	15	94	11	73	13	81	7	100	1	33	4	80
4	AAE20CS005	ALIYA FATHIMA	17	74	18	90	11	69	10	67	15	94	5	71	3	100	5	100
5	AAE20CS006	ANJANA SANTHOSH	20	87	16	80	13	81	14	93	15	94	7	100	3	100	5	100
6	AAE20CS008	FAMITHA P S	13	57	11	55	11	69	11	73	11	69	4	57	3	100	3	60
7	AAE20CS010	FARVEEN FATHIMA	17	74	15	75	10	63	10	67	14	88	4	57	3	100	5	100
8	AAE20CS011	FATHIMA BEEVI	7	30	5	25	5	31	5	33	7	44	2	29	1	33	2	40
9	AAE20CS012	HIJAS	15	65	18	90	13	81	10	67	11	69	5	71	3	100	3	60
10	AAE20CS014	MERIN VARGHESE	21	91	13	65	15	94	13	87	16	100	6	86	2	67	5	100
11	AAE20CS015	MOHAMMAD RAZIN RASHEED	12	52	10	50	10	63	10	67	10	63	4	57	3	100	3	60
12	AAE20CS016	MOHAMMED AMEEN	15	65	12	60	12	75	10	67	13	81	5	71	3	100	3	60
13	AAE20CS017	MUHAMMED ASLAM M A	12	52	11	55	11	69	11	73	13	81	5	71	3	100	3	60
14	AAE20CS018	MUHAMMED SALIH P K	16	70	13	65	13	81	10	67	14	88	6	86	2	67	3	60
15	AAE20CS020	NELVIN THANKACHAN	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
16	AAE20CS021	RAIBAN FATHIMA	19	83	16	80	14	88	14	93	16	100	6	86	3	100	5	100
17	AAE20CS022	RINSHA SHAJAHAN	12	52	18	90	11	69	8	53	11	69	4	57	2	67	2	40
18	AAE20CS023	ROHITH R	14	61	10	50	8	50	11	73	12	75	4	57	3	100	3	60
19	AAE20CS024	SHAHZAD ALI A N	15	65	11	55	11	69	10	67	13	81	5	71	2	67	3	60
20	AAE20CS025	SHANIYA SHAMSUDEEN	11	48	6	30	10	63	7	47	11	69	4	57	2	67	0	0
21	AAE20CS027	SHOBITH RAJESH	11	48	8	40	8	50	9	60	11	69	4	57	3	100	2	40
22	AAE20CS028	SNEHA G NATH	13	57	11	55	11	69	9	60	12	75	4	57	1	33	1	20
23	AAE20CS029	THANVEER THAJUDEEN	14	61	15	75	11	69	10	67	10	63	5	71	3	100	3	60
24	AAE20CS030	THOUFEEK S	15	65	15	75	12	75	14	93	14	88	5	71	3	100	4	80

Class Advisor: Deniya

HOD:

Principal:

SL NO	SUBJECT CODE	SUBJECT NAME	Faculty
1	CST 301	Formal Languages and Automata Theory	Deniya Varghese
2	CST 303	Computer Networks	Banu Sumayya S
3	CST 305	System Software	Anju Pathrose
4	CST 307	Microprocessors and Microcontrollers	Kala O S
5	CST 309	Management of Software Systems	Boby Jose
6	MCN 301	Disaster Management	Shahanas Muhammed
7	CSL 333	Database Management Systems Lab	Banu Sumayya S
8	CSL 331	System Software and Microprocessor Lab	Kala O S

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Continuous Assessment Test I : November 2022

Sem : S5	Program : B.Tech CSE	Course Code : CST305	Total Marks : 50	Time : 1 30Hrs.
Course Name : SYSTEM SOFTWARE				

CO : Course Outcome no **Level :** *Revised Bloom's taxonomy level no. (1-6)*

Co:	Level:	Question No:	
PART A Answer all questions (4 marks each)			
1	2	1	Differentiate between System Software and Application Software
1	1	2	Explain the addressing mode of SIC machine
1	1	3	What are Assembler Directives? Explain with examples.
2	2	4	Explain the Data Structure used in implementation of Assemblers
2	2	5	What is a forward reference?
PART B			
1 1	1 3	6	a)Explain about different System Software (9 Marks) b)Explain the difference between i) A1 RESW 3 and A1 WORD 3 ii)B BYTE C'23' and B BYTE X'23' iii) END and END LABEL (6 Marks)
OR			
2	3	7	Write an SIC/XE program to read a 100- byte record from an input device to memory using subroutine (15 Marks)
1	2	8	Explain about SIC/XE machine architecture. (15 Marks)
OR			
2 2	1 3	9	a)Explain about the Header record, Text record and End record of object program. (6 Marks) b)Write down and explain the algorithm for Pass 1 of assembler. (9 Marks)

*CO1:Distinguish softwares into system and application software categories

*CO2: Identify standard and extended architectural features of machines

Bloom's Taxonomy Levels:

1. *Remember*
2. *Understand*
3. *Apply*
4. *Analyze*
5. *Evaluate*
6. *Create*

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Continuous Assessment Test I: November 2022

Scheme of Evaluation

Sem : S5	Program : B.Tech CSE	Course Code :CST305	Total Marks : 50	Time : 1 30Hrs.
Course Name : System Software				

CO: Course Outcome no *Level: Revised Bloom's taxonomy level no. (1-6)*

PART A					
Co :	Level :	Question No:	Points	Marks	Total Marks
1	2	1	**System Software** is the type of software that is the interface between application software and system. Low-level languages are used to write the system software. System Software maintains the system resources and gives the path for application software to run. An important thing is that without system software, the system can not run. It is a general-purpose software. **Application Software** is the type of software that runs as per user request. It runs on the platform which is provided by system software. High-level languages are used to write the application software. It's a specific purpose software. The main difference between System Software and Application Software is that without system software, the system can not run on the other hand without application software, the Low-level maintains system always runs.	2 2	4
1	1	2	Direct Addressing Explanation with example Indexed Addressing Explanation with example	1 1 1 1	4
1	1	3	Assembler directives (or pseudo instructions) are not translated into machine instructions. Instead, they provide instructions to the assembler itself. START Specify name and starting address for the program. END Indicate the end of the source program and (optionally) specify the first executable instruction in the program. BYTE Generate character or hexadecimal constant occupying as many bytes as needed to represent the constant. WORD Generate one-word integer constant. RESB Reserve the indicated number of bytes for a data area. RESW Reserve the indicated number of words for a data area.	2 2	4
2	2	4	SYMTAB OPTAB	2	4

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			LOCCTR	1 1	
2	2	5	A forward reference **occurs when a label is used as an operand, for example as a branch target, earlier in the code than the definition of the label**. The assembler cannot know the address of the forward reference label until it reads the definition of the label.	4	4
			PART B		
1	a:1	6a)	• **Operating system:** Harnesses communication between hardware, system programs, and other applications. • **Device driver:** Enables device communication with the OS and other programs. • **Firmware:** Enables device control and identification. • **Translator:** Translates high-level languages to low-level machine codes. • **Utility:** Ensures optimum functionality of devices and applications.	a)5	15
1	b:3	6b)	Explanation with Example Explanation with Example	b)4 b)6	
			OR		
2	3	7	Program	15	15
1	2	8	Addressing mode Instruction format Registers Input and output	5 5 3 2	15
			OR		
2	a:1	9a)	Format of Header record Format of Text record Format of End record	a)2 a)2 a)2	15
2	b:3	9b)	Algorithm Explanation	b)6 b)3	

*CO1: Distinguish softwares into system and application software categories

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1.Remember

2.Understand

3.Apply

4.Analyze

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38½/50

muhammed salih pk.
CSE ROLL NO.14
S5
SYSTEM SOFTWARE

SYSTEM SOFTWARE

9, b, b algorith for pass 1 of assembler

```
begin
    read the first input line
    if OPCODE = 'START' then
begin
    SAVE #[OPERAND] as starting address
    initialize LOCCTR to starting address
    write line to intermediate file
    read the next input line
end {if START}
else
    initialize LOCCTR to 0
while OPCODE ≠ 'END' do
begin
    This is not COMMENT line then
begin
    there is SYMBOL in the LABEL field then
begin
    search SYMTAB for LABEL
    if found then
        set error flag (duplicate symbol)
    else
        insert (LABEL, LOCCTR) to SYMTAB
end {if symbol}
search OPTAB for OPCODE
```

```
ADD 3 to LOCCTR
ELSE IF OPCODE = 'RESW' THEN
ADD 3* #[OPERAND] TO LOCCTR
else if OPCODE = 'RESB' then
  ADD #[OPERAND] TO LOCCTR
  else if OPCODE = 'BYTE' then
begin
    find the length of constant byte
    ADDlength to LOCCTR.
END{if BYTE}
ELSE
    Set error flag (Invaild comment' CODES)
END{COMMENT NOT END}
    write line to intermediate file
    read next instruction line
END{while not END}
    write last line to intermediate file
    SAVE (LOCCTR - starting address) to
                                length of program
END {pass 1}
```

pass 1 of assembler begin and take the first input line so if OPcode start begin save the operand as starting adding initialize LOCCTR to starting address write the line in intermediate file and read the next input line and continue till the start is end else LOCCTR as 0 while the opcode ≠ end do and

Add 3 to LOCCTR else other word 3 to LOCCTR or RESW 3* #[operand] to or RESB #[operand] to BYTE length to LOCCTR end. Byte and other write to intermediate file end. Save LOCCTR - starting address length of code.

9,9) Object program is format of object their is format of all record and object program has 3 record Header record, text record, & END record

Object program has a program that has three records.

~~Header record~~

format of these record.

Header record.

Col 1 H

Col 2-7 Program name

Col 8-13 Starting address of byte (hexadecimal)

Col 14-19 length of the record in byte (hexadecimal)

b

TEXT record

Col 1 T

Col 2-7 Starting address of its record byte (hexadecimal)

Col 8-9 length of the its record byte (hexadecimal

Col 9-69 address of the byte

END record

Col 1 E

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7, SIC/XE read a 100-byte record from an input device to memory using subroutine

```
        JSUB   READ
        .
READ    LDX    #0
        LDT    #100
RLOOP   TD     INDEV
        JEQ    RLOOP
        RD     INDEV
        STCH   RECORD,X
        TIX    T
        JLS    RLOOP
        RSUB
        .
        .
INDEV   BYTE   X'F1'
RECORD  RESB   100
```

2, Addressing Mode of SIC machine (direct & indexed)

1. direct addressing mode.

n i x b p

n = indirect
i = immediate
x = index
b = base relative
p = program counter

neither immediate nor indirect

This address mode is direct use without any use of register the data are directly address.

immediate Addressing mode..

3. Indirect addressing mode

In this addressing mode is indirectly address the memory location address is in program.

n	i	x	b	p
1	0	0	0	0

eg:- LDA, [805]

4. Index addressing mode.

In this addressing mode index is used

n	i	x	b	p
1	1	1	0	0

neither immediate indirect

5. Relative addressing mode

here 2 addressing modes

base relative addressing modes & program counter addressing modes,

base relative addressing mode b=1 p=0 TA = Displacement + (B)

program counter addressing mode b=0 p=1 TA = Displacement + PC

base relative index addressing mode b=1 p=0 x=1 TA = Displacement + (B) + (X)

program counter addressing mode

3, Assembler Directives

Assembler Directives is code that are not translated into machine code the CODE is used for itself.

1. START

- The START assembler Directives to use START the operations in Assembler

eg:-

START LDX #05

2. END

The END assembler Directives is used for END the assembler

eg:-

END START

3. WORD

The WORD assembler Directives is used a one WORD in the memory location

ZERO WORD 0

↓ ↓

label value

4. BYTE

the BYTE assembler Directives is used a one BYTE in the memory

5. RESW

The RESW is Assembler Directives is used to reserve no. of word in a memory location.

eg:- RECORD RESW 100

6. RESB

The RESB is Assembler Directives is used for reserve no. of byte in memory location

eg:- RECORD RESB 100.

1. System software

System software are used in computer system to connect software and hardware.

- System software are in need in system operation system performance.
- System software are working to enable operating system
- System software is main of computer system.
- suppose system software is weak it affect all over the computer system
- System software is uses to perform the system functions.

Application software

- Application software uses in a application / software
- Application software uses to perform the operation of application / software.
- Application software operate in the operating system
- Application software is not main in the computer system
- suppose a application software is crush it not affect operating system, system software, computer system.
- Application software used for the application function and application operation.

5. Forward reference.

The forward reference a label is used in the assembler the label ends the by forward reference. the operation of label

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1	AAE20CS001	ADITYA SURESH	3	20	26	2	12	17
2	AAE20CS002	AKHIL NATH T M	14	15	34	AB	27	25
3	AAE20CS004	ALFIYA E S	11	9	18	15	8	9
4	AAE20CS005	ALIYA FATHIMA	30	14	12	21	AB	14
5	AAE20CS006	ANJANA SANTHOSH	AB	23	26	13	38	16
6	AAE20CS008	FAMITHA P S	2	15	11	11	14	21
7	AAE20CS010	FARVEEN FATHIMA	29	27	26	22	AB	22
8	AAE20CS011	FATHIMA BEEVI	4	4	13	5	AB	Ab
9	AAE20CS012	HIJAS	0	8	22	15	12	17
10	AAE20CS014	MERIN VARGHESE	20	AB	AB	AB	AB	38
11	AAE20CS015	MOHAMMAD RAZIN RASHEED	0	10	20	20	12	12
12	AAE20CS016	MOHAMMED AMEEN	1	21	25	22	12	6
13	AAE20CS017	MUHAMMED ASLAM M A	1	7	5	0	3	6
14	AAE20CS018	MUHAMMED SALIH P K	45	31	39	25	25	Ab
15	AAE20CS020	NELVIN THANKACHAN	AB	AB	AB	AB	AB	Ab
16	AAE20CS021	RAIBAN FATHIMA	10	15	14	29	20	Ab
17	AAE20CS022	RINSHA SHAJAHAN	16	22	15	14	20	12
18	AAE20CS023	ROHITH R	22	20	9	AB	21	Ab
19	AAE20CS024	SHAHZAD ALI A N	20	20	12	24	15	20
20	AAE20CS025	SHANIYA SHAMSUDEEN	4	21	3	30	AB	8
21	AAE20CS027	SHOBITH RAJESH	2	16	10	AB	33	21
22	AAE20CS028	SNEHA G NATH	4	22	8	18	23	8
23	AAE20CS029	THANVEER THAJUDEEN	5	20	7	15	20	Ab
24	AAE20CS030	THOUFEEK S	30	26	13	28	34	Ab

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

Continuous Assessment Test II : December 2022

Sem : S5	Program : B.Tech CSE	Course Code : CST305	Total Marks : 50	Time : 1 30Hrs.
Course Name : SYSTEM SOFTWARE				

CO : Course Outcome no *Level : Revised Bloom's taxonomy level no. (1-6)*

Co :	Level :	Question No:	
			PART A **Answer all questions (4 marks each)**
3	2	1	What is a load and go assembler?
3	1	2	Differentiate between control sections and program blocks with the help of an example.
3	1	3	Explain any one example of assembler implementation.
4	2	4	What is the use of bitmask in program relocation? Illustrate with example.
4	2	5	Give the algorithm for an absolute loader.
			PART B
3	1	6	a) Explain program relocation with examples. (9 Marks)
3	3		b) Is there a need to use modification records for the given SIC/XE program segment? Explain your answer. If yes, show the contents of modification record. 0000 COPY START 0 ..... 0006 +JSUB RDREC 000A LDA LENGTH ..... 0033 LENGTH RESW 1 ..... 1036 RDREC CLEAR X (6 Marks)
			OR
4	3	7	Explain about machine-dependent loader features (15 Marks)
3	2	8	a)What is a multi pass assembler? (7 marks)

Relocation, Program Linking, Algo, data structure of a linking loader

M, 0007, 05, +RDREC

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(20)

3	2		b) Employ multipass assembler to evaluate the following expressions. Expression No. | Loc | Source Statement 1 | | HALFSZ EQU MAXLEN/2 500 2 | | MAXLEN EQU BUFEND-BUF 1000 3 | | PREVBT EQU BUFFER-1 4033 4 | 4034 | BUFFER RESB 4096 5 | 5034 | BUFEND EQU * (8 Marks)
			OR
4 4	1 3	9	a) Given an idle computer with no programs in memory, how do we get things started . Explain with algorithm. (6 Marks) b)Explain machine-independent loader features. (9 Marks)

*CO3: Identify machine dependent features of system software

*CO4: Identify machine independent features of system software.

*CO5: Design algorithms for system softwares and analyze the effect of data structures.

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1. *Remember*
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Scheme of Evaluation

Sem : S5	Program : B.Tech CSE	Course Code :CST305	Total Marks : 50	Time : 1 30Hrs.
Course Name : System Software				

CO: Course Outcome no *Level: Revised Bloom's taxonomy level no. (1-6)*

PART A					
Co:	Level:	Question No:	Points	Marks	Total Marks
3	2	1	One pass assembler that generates their object code in memory for immediate execution is known as load and go assembler. Here no object programmer is written out and hence no need for loader.	4	4
3	1	2	A control section is a part of the program that maintains its identity after assembly; each control section can be loaded and relocated independently of the others. Different control sections are most often used for subroutines or other logical subdivisions. Program blocks allow the generated machine instructions and data to appear in the object program in a different order by separating blocks for storing code, data, stack, and larger data block.	2 2	4
3	1	3	any one example of assembler implementation	4	4
4	2	4	The relocation bits are gathered together following the length indicator in each text record and which is called as bit mask. For e.g. the bit mask FFC(111111111100) specifies that the first 10 words of object code are to be modified during relocation.	4	4
4	2*	5	Algorithm	4	4
PART B					
3	a:1	6a)	Relocation is the process of connecting symbolic references with symbolic definitions. For example, when a program calls a function, the associated call instruction must transfer control to the proper destination address at execution. In other words, relocatable files must have information that describes how to modify their section contents, thus allowing executable and shared object files to hold the right	9	

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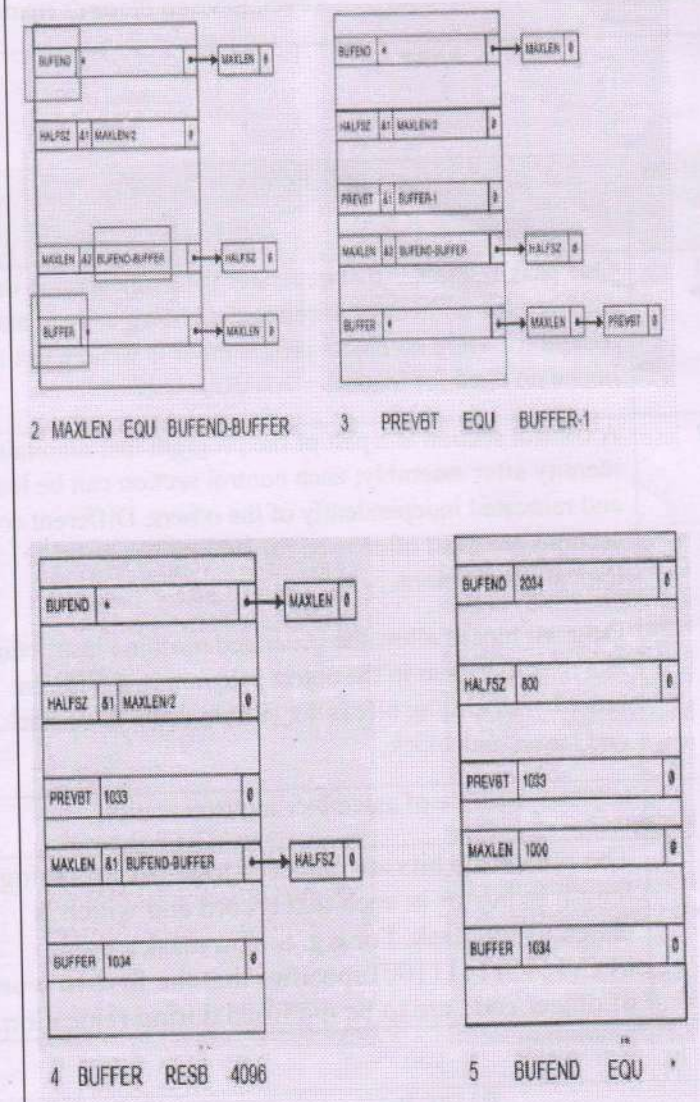
3	b:3	6b)	information for a process's program image. Relocation entries are these data. Yes. For the address 0006	6	15
			OR		
4	3	7	Explain about machine-dependent loader features	15	15
3 3	a:2 b:2	8a) 8b)	Explanation of a a multi pass assembler  	7 8	15
			OR		
4 4	a:1 b:3	9a) 9b)	Bootstrap loader Explanation of machine-independent loader features.	6 9	15

*CO3: Identify machine dependent features of system software

*CO4: Identify machine independent features of system software.

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*CO5: Design algorithms for system softwares and analyze the effect of data structures.

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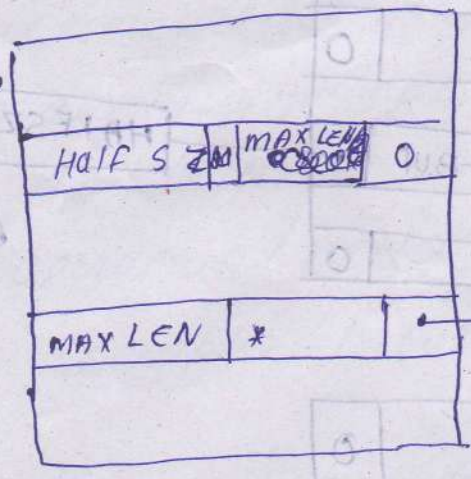
muhammed Salih PK
CSE 14 55
System Software

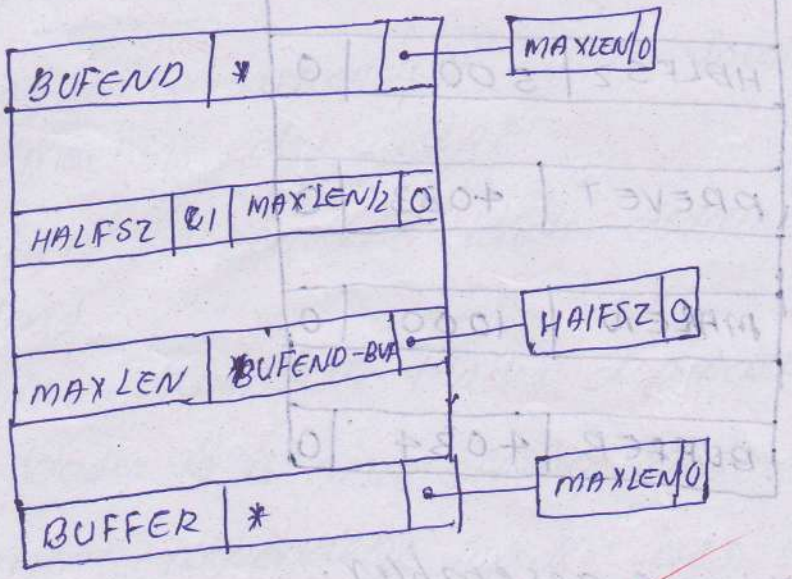
20½
50

8, b,

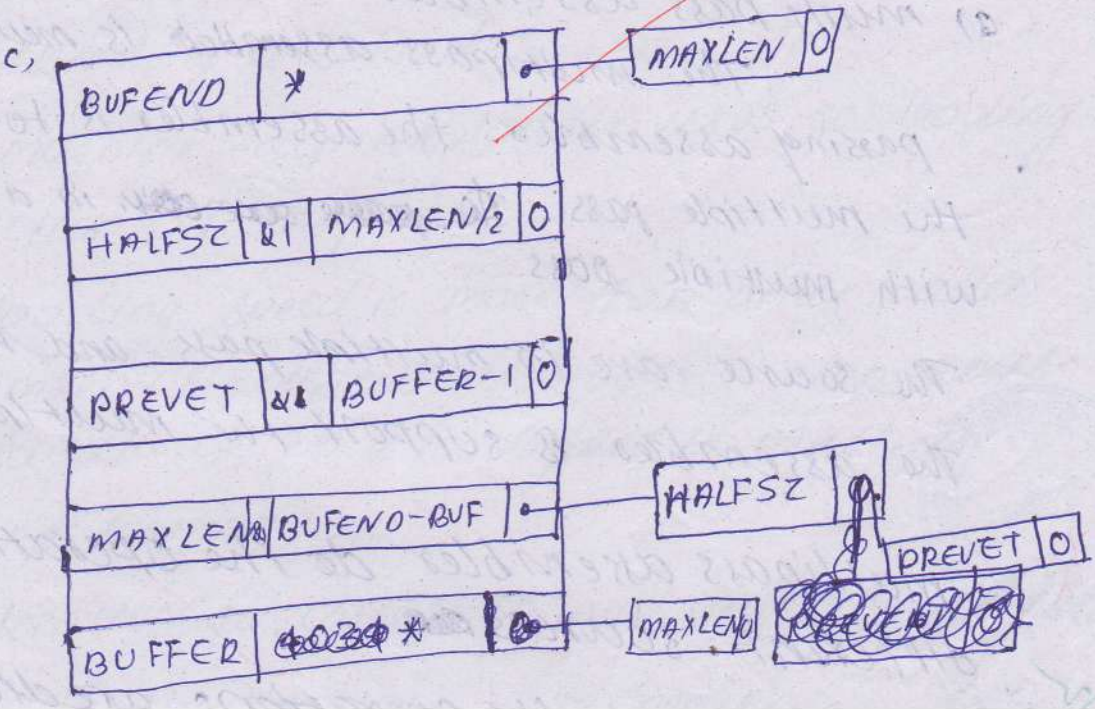
a)



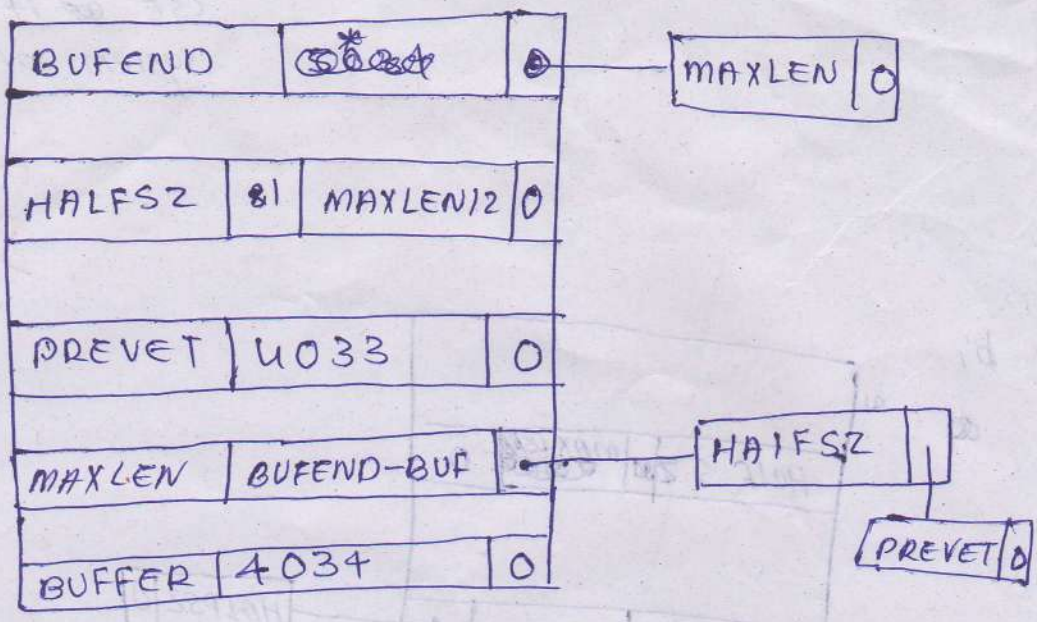
b.



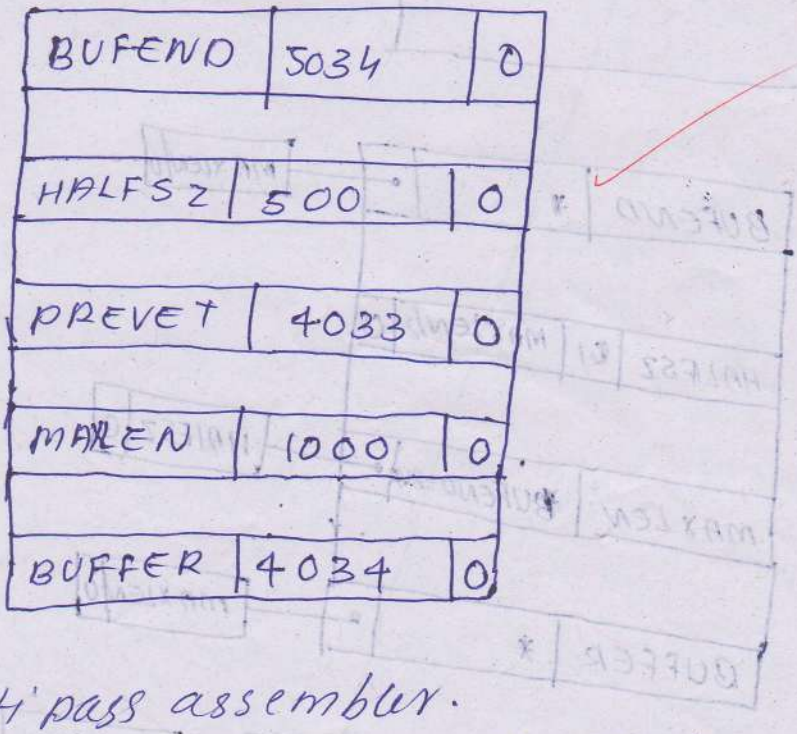
c)



d)



e)



8

a) multi pass assembler.

The multipass assembler is multiple passing assembler. the assembler is to execute the multiple pass. ~~The pass are~~ in a system with multiple pass

The source are in multiple pass and location the assembler is support the multiple pass

multipass assembler do the operation from different sources

In the multipass assembler the source are memory space reserved are in different pass. If all the pass is in multipass assembler. the assembler call the different source from different pass and operation / execution complete by assembler This assembler is called multipass assembler.

7. machine - dependent loaders

The machine - dependent loader loading with machine - dependent

- machine - dependent loader dependent machine and load
- machine - dependent loader dependent the machine

So the loader is more efficiently work.

- machine - dependent loader uses so any kind machine to load
- machine - dependent loader uses so loading function is easy
- loading speed is more than other loader
- machine - dependent loader it can suitable with loading to machine.
- no waiting time
- location get correct so load properly and correctly

instruction in record

- So we uses relocation bit. All the instruction are specified with relocation bit

The relocation bit are 3 hexadecimal ~~symbolic~~ represent in text record

The relocation bit is ~~fixed~~ 12 bit

relocation bit are write with bit maping lengt

Text-record

01 – Text record

2 - 7 → starting address

8 - 9 → length (byte)

11 - 12 → relocation bit

13 - 72 → Opcode.

4

In the program relative address is not uses so we can modify all string we cant do all instruction write in record so uses relocation bit

relocation bit is only 3 hexadecimal

one of instruction are 4 bit

eg:- suppose.

T, 000014, 05, 0EA001

↓ relocation bit

1. load and go assembler.

The Assembler loaad and go is means that assembler load the instruction and go to back.

The assembler is load the source and go back

2 control sections

. control sections needs indepently

. different control section for derivation of all

. theynot use same control section

. create new new control section for each program block

. control sections needs indepently but program block not in indepently

2½

. no different program block

. program block deriving in one of the program

. they use same program block

. not need for different program block

. not create a new program block for each.

5. Absolute loader

Absolute loader is loader to load the instruction to the system

1

eg:- Boot strap loader
- load when system boot
first load operating system by boot strap loader.

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

S5 CSE 2020-2024 Batch

CONTINUOUS ASSESSMENT TEST II : December 2022

Sl.No:	Register Number	Student Name	CST 301 Total Marks: 50	CST 303 Total Marks: 50	CST 305 Total Marks: 50	CST 307 Total Marks: 50	CST 309 Total Marks: 50	MCN 301 Total Marks: 50
1	AAE20CS001	ADITYA SURESH	15	10	2	3	26	Ab
2	AAE20CS002	AKHIL NATH T M	27	23	15	27	34	25
3	AAE20CS004	ALFIYA E S	14	13	13	9	23	10
4	AAE20CS005	ALIYA FATHIMA	28	9	16	22	24	23
5	AAE20CS006	ANJANA SANTHOSH	3	AB	10	14	28	9
6	AAE20CS008	FAMITHA P S	9	9	7	15	AB	14
7	AAE20CS010	FARVEEN FATHIMA	17	20	16	32	32	30
8	AAE20CS011	FATHIMA BEEVI	0	3	0	3	15	8
9	AAE20CS012	HIJAS	14	14	11	5	20	13
10	AAE20CS014	MERIN VARGHESE	32	38	28	27	36	38
11	AAE20CS015	RASHEED	16	4	15	16	33	6
12	AAE20CS016	MOHAMMED AMEEN	22	9	20	16	20	12
13	AAE20CS017	MUHAMMED ASLAM M A	0	2	8	6	20	2
14	AAE20CS018	MUHAMMED SALIH P K	34	30	21	33	31	42
15	AAE20CS020	NELVIN THANKACHAN	AB	AB	AB	AB	AB	Ab
16	AAE20CS021	RAIBAN FATHIMA	AB	AB	9	7	23	Ab
17	AAE20CS022	RINSHA SHAJAHAN	2	10	20	3	29	Ab
18	AAE20CS023	ROHITH R	AB	AB	AB	AB	22	Ab
19	AAE20CS024	SHAHZAD ALI A N	11	13	1	11	20	7
20	AAE20CS025	SHANIYA SHAMSUDEEN	1	6	1	4	26	Ab
21	AAE20CS027	SHOBITH RAJESH	2	26	6	17	29	11
22	AAE20CS028	SNEHA G NATH	1	7	4	12	20	Ab
23	AAE20CS029	THANVEER THAJUDEEN	2	5	6	11	25	15
24	AAE20CS030	THOUFEEK S	14	22	16	18	AB	Ab

SL NO	SUBJECT CODE	SUBJECT NAME	Faculty
1	CST 301	Formal Languages and Automata Theory	Deniya Varghese
2	CST 303	Computer Networks	Banu Sumayya S
3	CST 305	System Software	Anju Pathrose
4	CST 307	Microprocessors and Microcontrollers	Kala O S
5	CST 309	Management of Software Systems	Boby Jose
6	MCN 301	Disaster Management	Shahanas Muhammed

Class Advisor:

HOD:

Principal:

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

S5 CSE 2020-2024 Batch

CONSOLIDATED ATTENDANCE & INTERNAL 2022

Sl.No:	Register Number	Student Name	CST 301		CST 303		CST 305		CST 307		CST 309		MCN 301		CSL 333		CSL 331	
			Percentage	Internal	Percentage	Internal	Percentage	Internal	Percentage	Internal	Percentage	Internal	Percentage	Internal	Percentage	Internal	Percentage	Internal
1	AAE20CS001	ADITYA SURESH	75	37	75	36	78	35	78	31	78	36	76	35	80	55	77	48
2	AAE20CS002	AKHIL NATH T M	75	37	75	37	84	45	78	41	86	42	80	37	90	67	75	54
3	AAE20CS004	ALFIYA E S	82	36	80	35	96	40	82	36	93	38	92	36	80	54	85	51
4	AAE20CS005	ALIYA FATHIMA	84	44	86	35	84	39	84	41	96	40	84	35	90	65	92	64
5	AAE20CS006	ANJANA SANTHOSH	78	36	84	39	88	43	78	37	86	44	80	35	90	57	92	55
6	AAE20CS008	FAMITHA P S	75	36	76	35	78	33	82	37	78	35	76	35	100	61	85	57
7	AAE20CS010	FARVEEN FATHIMA	87	39	84	39	84	41	86	41	96	43	80	37	100	66	92	65
8	AAE20CS011	FATHIMA BEEVI	82	36	76	28	82	34	84	35	78	36	76	30	80	46	77	50
9	AAE20CS012	HIJAS	80	35	78	35	86	37	75	34	75	35	76	35	100	65	77	57
10	AAE20CS014	MERIN VARGHESE	93	40	84	40	90	42	86	43	96	45	92	44	90	58	85	62
11	AAE20CS015	MOHAMMAD RAZIN RASHEED	76	37	75	35	84	41	76	35	78	36	76	35	100	69	77	54
12	AAE20CS016	MOHAMMED AMEEN	82	37	75	38	92	41	78	36	78	35	76	35	100	68	77	50
13	AAE20CS017	MUHAMMED ASLAM M A	75	35	75	35	76	35	76	31	90	33	76	30	80	47	77	45
14	AAE20CS018	MUHAMMED SALIH P K	80	45	78	41	86	44	84	44	91	43	84	44	90	60	77	61
15	AAE20CS020	NELVIN THANKACHAN	0	0	0	0	NA	NA	0	0	0	0	0	0	0	0	0	0
16	AAE20CS021	RAIBAN FATHIMA	80	38	86	35	88	41	90	36	98	40	76	35	100	66	92	62
17	AAE20CS022	RINSHA SHAJAHAN	80	38	78	37	80	37	76	32	78	38	76	35	80	56	77	51
18	AAE20CS023	ROHITH R	75	39	75	36	76	35	75	34	80	37	76	33	80	56	75	55
19	AAE20CS024	SHAHZAD ALI A N	75	36	75	35	76	39	75	35	84	36	76	35	80	56	77	55
20	AAE20CS025	SHANIYA SHAMSUDEEN	75	34	75	34	75	36	75	38	75	33	76	30	80	46	75	45
21	AAE20CS027	SHOBITH RAJESH	75	38	75	38	76	36	76	36	83	42	76	35	80	59	75	48
22	AAE20CS028	SNEHA G NATH	75	36	75	36	76	36	75	34	86	38	76	35	80	53	75	45
23	AAE20CS029	THANVEER THAJUDEEN	78	37	78	37	78	34	80	33	91	38	80	35	80	57	77	55
24	AAE20CS030	THOUFEEK S	76	39	75	39	82	41	80	41	78	41	76	43	80	57	77	57

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5	CST 309	Management of Software Systems	Boby Jose
6	MCN 301	Disaster Management	Shahanas Muhammed
7	CSL 333	Database Management Systems Lab	Banu Sumayya S
8	CSL 331	System Software and Microprocessor Lab	Kala O S

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING
SEMESTER : S4 **BATCH:2020-2024**

University Examination Result Analysis

Sl No	REGISTER NUMBER	NAME OF STUDENT	MAT206	CST202	CST204	CST206	HUT200	MCN202	CSL202	CSL204
1	AAE20CS001	Aditya Suresh	F	F	D	F	P	P	F	P
2	AAE20CS002	Akhil Nath T M	C	B	C	C+	B	C	C+	C+
3	AAE20CS004	Alfiya E S	F	F	F	F	F	F	D	F
4	AAE20CS005	Aliya Fathima	C+	D	C	F	D	D	A+	D
5	AAE20CS006	Anjana Santhosh	F	P	D	F	D	D	F	F
6	AAE20CS008	Famitha P S	F	F	F	F	F	F	F	B
7	AAE20CS010	Farveen Fathima	A	D	B	P	C	D	S	F
8	AAE20CS011	Fathima Beevi	F	F	F	F	F	F	F	B
9	AAE20CS012	Hijas	F	F	F	F	F	F	F	F
10	AAE20CS014	Merin Varghese	B	P	C+	F	B+	B+	A	B+
11	AAE20CS015	Mohammad Razin Rasheed	F	F	F	F	D	D	F	F
12	AAE20CS016	Mohammad Ameen	F	F	F	F	C+	D	F	F
13	AAE20CS017	Muhammad Aslam M A	F	F	F	F	F	P	F	F
14	AAE20CS018	Muhammad Salih P K	C	C	D	C	B+	B+	A+	A
15	AAE20CS020	Nelvin Thankachan	FE	FE	FE	FE	ABSENT	F	ABSENT	ABSENT
16	AAE20CS021	Raiban Fathima	F	F	P	F	F	P	A	D
17	AAE20CS022	Rinsha Shajahn	F	Withheld	F	Withheld	C	D	Withheld	Withheld
18	AAE20CS023	Rohith R	D	P	F	F	D	D	C+	D
19	AAE20CS024	Shahzad Ali A N	F	F	D	F	D	D	P	F
20	AAE20CS025	Shaniya Shamsudheen	F	FE	FE	FE	P	D	F	F
21	AAE20CS027	Shobith Rajesh	C+	P	F	F	B	C+	C+	P
22	AAE20CS028	Sneha G Nath	F	ABSENT	F	F	F	F	F	D
23	AAE20CS029	Thanveer Thajudeen	F	F	P	F	F	D	A+	D
24	AAE20CS030	Thoufeek S	C	P	C	P	B+	A+	A+	D

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Name of Faculty	Subject	No of Students Registered	No of Students Passed	Pass Percentage
Anjali Prasad K P	MAT206 GRAPH THEORY	23	8	35
Banu Sumayya S	CST202 COMPUTER ORGANISATION AND ARCHITECTURE	22	9	41
Boby Jose	CST204 DATABASE MANAGEMENT SY	22	11	50
Asla M P	CST206 OPERATING SYSTEMS	22	4	18
Boby Jose	HUT200 PROFESSIONAL ETHICS	24	15	63
Shahanas Muhammed	MCN202 CONSTITUTION OF INDIA	24	18	75
Banu Sumayya S	CSL202 DIGITAL LAB	24	12	50
Asla M P	CSL204 OPERATING SYSTEMS LAB	24	13	54

Number of Students Registered	24
Number of Students Passed	3
Total Pass Percentage	13%

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